

REMARKS

Claims 21-41 are pending. The Examiner's reconsideration of the rejections is respectfully requested in view of the amendments and remarks.

The specification has been objected to, wherein the Examiner indicated that the Abstract included more than 150 words. The Abstract of the Disclosure has been amended to include less than 150 words. Entry of the amendment and reconsideration of the objection is respectfully requested.

Claims 21-41 have been rejected under 35 USC 102(a) as being anticipated by "Space and Time Efficient Memory Layout for Multiple Inheritance" Joseph Gil et al. 1999 (hereinafter Gil). The Examiner stated essentially that Gil teaches all of the limitations of Claims 21-41.

Respectfully, the present application has an effective filing date of October 22, 1999 by virtue of parent Patent 6,754,887 - the present application is a Continuation Application of Patent 6,754,887. The cited reference has a publication date of November 1999 in the *Proceedings of Object-Oriented Programming, Systems, Languages and Applications (OOPSLA'99)*, (Denver, CO). Therefore, the reference is not prior art as against the present application. Reconsideration of the rejection is respectfully requested.

For the forgoing reasons, the present application, including Claims 21-41, is believed to be in condition for allowance. The Examiner's early and favorable action is respectfully urged.

Respectfully submitted,

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